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PATENT NUMBER and  
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## U.S. UTILITY Patent Application

APPL NUM 10055896	FILING DATE 01/28/2002	CLASS 257	SUBCLASS 295	GAU 2811	EXAMINER NGUYEN
<b>**APPLICANTS:</b> Tohda Toshiyuki; NGUYEN					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-029493 02/06/2001					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 925-222	
Verified and Acknowledged Examiners's initials					
TITLE : Semiconductor device and process for manufacturing the same					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claims for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drawn	Figs. Drawn
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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